3^{RD} SEM./AI & ML./CS & E./IT/ 2024(W)

TH1 Computer System & Architecture

	Fi	ull M	Answer any five Questions including Q No.1& 2 Figures in the right hand margin indicates marks	3 Hrs
	1.		Answer All questions	2 x 10
		a.	Define Hit ratio.	
		b.	What do you mean by RISC and CISC?	
		c.	Define throughput of a system.	
		d.	What is program counter?	
		e.	Write down the functions of ALU.	
		f.	Define USB.	
		g.	What is opcode and operand?	
		h.	What is opcode and operand? Write down two examples of peripheral devices. Write an expression for speed up in pipeline.	
		i.	Write an expression for speed up in pipeline.	
		j.	Define SCSI.	
	2.		Answer Any Six Questions	5 x 6
		a.	Explain difference types of instruction format with example.	
		b.	Compare between I/O mapped I/O and Memory mapped I/O.	
		c.	How an instruction is executed? Explain the steps of each cycle.	
		d.	Explain working principle of cache memory.	
		e.	Define Bus. Write the functions of data bus, address bus and control bus.	
		f.	Distinguish between hardwired control and micro programmed control.	
		g.	Explain the working principle of DMA.	
	3.		Answer Any Three Questions What do you mean by addressing modes? Explain all types of addressing mode.	10
-01-7	4.		Define parallel processing. Briefly describe Flynn's classification.	10
57.0	5.		Explain memory hierarchy with suitable diagram.	10
,-	6.		Write short notes on I. Virtual memory II. Interrupt driven I/O	5+5
	7		Evoluin the hasic functional units of a computer with suitable diagram	10